## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re the Application of: IIDA, Kenji

Group Art Unit:2841

Serial No.: 10/612,990

Examiner: Jeremy C. NORRIS

Filed: **July 7, 2003** 

P.T.O. Confirmation No.: 1625

For: MULTILAYER CIRCUIT BOARD AND METHOD OF MANUFACTURING THE SAME

## **NOTICE OF APPEAL**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: November 21, 2005

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated May 19, 2005 of the Primary Examiner.

The item(s) checked below are appropriate:

- 1. <u>X</u> A petition for a two-month extension of time to respond to the Final Rejection was filed on October 18, 2005.
- A timely response to the Final Rejection has been filed, as provided in 37 C.F.R. 1.113. 2. \_\_
- 3. X Fee - \$500.00
  - X Enclosed
    - Not required (fee paid in prior appeal)
  - Charge to Deposit Account No. 01-2340

In the event this Notice of Appeal is not considered to be timely filed, Applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2340, along with any other additional fees which may be required with respect to this Notice.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS

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PATENT TRADEMARK OFFICE